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## **BIB DATA SHEET**

## CONFIRMATION NO. 2472

						CONFIRMATION NO. 2472			
SERIAL NUM 10/587,74	DATE		` ′	CLASS 257		ROUP ART UNIT 2823		ATTORNEY DOCKET NO. 0756-7782	
		RULE							
APPLICANTS Shunpei Yamazaki, Tokyo, JAPAN; Koji Dairiki, Kanagawa, JAPAN;									
** CONTINUING DATA *******************************  This application is a 371 of PCT/JP05/01541 01/27/2005									
** FOREIGN APPLICATIONS ************************************									
** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 04/03/2007									
Foreign Priority claimed Yes No 35 USC 119(a-d) conditions met Yes No Met after Allowance			STATE OR COUNTRY	SHEETS	TOTAL CLAIMS		INDEPENDENT CLAIMS		
Verified and	MICHELE Examiners	FAN/	Allowance	JAPAN	30	60		5	
ADDRESS									
ERIC ROBINSON PMB 955 21010 SOUTHBANK ST. POTOMAC FALLS, VA 20165 UNITED STATES									
TITLE									
Method for manufacturing thin film integrated circuit, and element substrate									
						☐ All Fees			
	FEES: Authority has been given in Paper No to charge/credit DEPOSIT ACCOUNT No for following:				□ 1.16	1.16 Fees (Filing)			
					NT 1.17	1.17 Fees (Processing Ext. of time)			
						☐ 1.18 Fees (Issue)			
						☐ Other			
					☐ Cred	☐ Credit			